



Academic Personnel Short Profile / Short CV

University:	Frederick University
Surname:	Kyriacou
Name:	Costas
Rank/Position:	Professor
School:	Engineering
Department:	Electrical Engineering, Computer Engineering, and Informatics
Scientific Domain:	Computer Architecture, Parallel Processing and Smart Systems

Academic qualifications				
Qualification	Year	Awarding Institution	Department	Thesis title (Optional Entry)
PhD	2005	University of Cyprus	Computer Science	Data Driven Multithreading Using Conventional Control Flow Microprocessors
MSc	1987	Polytechnic University of New York, USA	Electrical Engineering	N/A
BSc	1985	Polytechnic Institute of New York, USA	Electrical Engineering	N/A

Employment history in Academic Institutions/Research Centers				
Period of employment		Employer	Location	Position
From	To			
2019	Present	Frederick University	Nicosia, Cyprus	Professor (Vice-Rector of Academic Affairs: 2019 - present)
2007	2018	Frederick University	Nicosia, Cyprus	Associate Professor

				(Head of Department of Computer Science and Engineering: 2007 -2011 and 2014 - 2018)
1988	2007	Frederick Institute of Technology	Nicosia, Cyprus	Faculty of the Department of Computer Science and Engineering

Key <u>refereed</u> journal papers, monographs, books, conference publications etc. (up to ten until December 2024)						
Ref. Number	Year	Title	Other authors	Journal and Publisher / Conference	Vol.	Pages
1	2023	Approximate Priority Hybrid 3DNoC Buffered-Bufferless Router	S Savva, K Tatas	Micromachines	14 (2)	335-345
2	2022	A moving liver phantom in an anthropomorphic thorax for SPECT MP imaging	S Panagi, A Hadjiconstanti, G Charitou, D Kaolis, I Petrou, Y. Parpottas	Physical and Engineering Sciences in Medicine	45 (1)	63-72
3	2020	Fuzzy Classification of OpenCL Programs Targeting Heterogeneous Systems	Ahmad Al-Zoubi, Konstantinos Tatas	Journal of Intelligent & Fuzzy Systems, IOS Press	39(5)	7189-7202
4	2018	Towards Dynamic Multi-task Scheduling of OpenCL Programs on Emerging CPU-GPU-FPGA Heterogeneous Platforms: A Fuzzy Logic Approach	Ahmad Al-Zoubi, Konstantinos Tatas	2018 IEEE International Conference on Cloud Computing Technology and Science (CloudCom)	--	247-250
5	2018	Data-driven thread execution on heterogeneous processors	Samer Arandi, George Matheou, Paraskevas Evripidou	International Journal of Parallel Programming	46(2)	198-224
6	2015	Paradigm Shift for Exascale Computing	George Matheou, Paraskevas Evripidou	3rd International Conference on Exascale Applications and Software	--	109-114
7	2013	A novel prototyping and evaluation framework for NoC-based MPSoC	Konstantinos Tatas, Kostas Siozios, Alexandros Bartzas, Dimitrios Soudris	International Journal of Adaptive, Resilient and Autonomic Systems (IJARAS)	4(3)	1-24
8	2008	Rapid prototyping of the data-driven chip-multiprocessor (D2-CMP) using FPGAs	Konstantinos Tatas, Paraskevas Evripidou, Stephan Wong	Parallel Processing Letters	18(2)	291-306

9	2006	Data-driven multithreading using conventional microprocessors	Paraskevas Evripidou, Pedro Trancoso	IEEE Transactions on Parallel and Distributed Systems	17(10)	1176-1188
10	2006	CacheFlow: Cache optimizations for data driven multithreading	Paraskevas Evripidou, Pedro Trancoso	Parallel Processing Letters	16(2)	229-244

Research Projects (up to ten until May 2021)				
Ref. Number	Date	Title	Funded by	Project Role
1	2016 - 2019	“HippoAct- Hippoactive: Development of a Smart Horse Therapy Emulator”	Ministry of Energy, Commerce, Industry and Tourism of Cyprus	Principal Investigator
2	2011 - 2014	TERAFLUX - Exploiting Dataflow Parallelism in Tera-Device Computing	7th Framework - Concurrent Tera-device Computing	Research Associate
3	2010 - 2012	SYStematic Design of 3D Multicore Architectures with NeTwork-on-chip Communication (SYSMANTIC)	Cyprus Research Promotion Foundation	Coordinator - Principal Investigator
4	2008 - 2011	Automatic Data Driven Multithreading on Homogeneous and Heterogeneous (Cell BE) Processors utilizing the GCC Compile	Cyprus Research Promotion Foundation	Principal Investigator
5	2005 - 2008	DDM-CMP: Data Driven Multithreading on a Chip Multiprocessor, Development, and Implementation	Cyprus Research Promotion Foundation	Principal Investigator
6	2005 - 2006	CMPs-based Network and Storage I/O Subsystems	EU - HiPEAC	Research Associate
7	2004 - 2007	New Guardrail System Design for Critical Collision Cases in the Highways	Cyprus Research Promotion Foundation	Research Associate
8	2001 - 2003	Energy Absorbing Systems	Cyprus Research Promotion Foundation	Research Associate
9	2000 - 2002	UV Solar Radiation and Skin Cancer	Cyprus Research Promotion Foundation	Research Associate
10	1999 - 2001	Production Process Development for Aluminium Alloys Through Utilisation of Recyclable Aluminum Reserves	Cyprus Research Promotion Foundation	Research Associate

Academic Consulting Services and/or Participation in Councils / Boards/ Editorial Committees

Ref. Number	Period	Organization	Title of Position or Service
1	2011 - 2015	Council of Ministers of the Government of Cyprus	President of the Governing Board of the “Foundation for the Management of European Lifelong Learning Programs”
2	2010 - 2017	Cyprus Ministry of Transport, Communications and Works	Member of the Space Advisory Committee (Communications and Technology area)
3	2009 - 2018	Government of Cyprus	Member of the Bologna Experts/European Higher Education Experts Team of Cyprus
4	2016 - 2017	Ministry of Education and Culture of Cyprus	Chair of a committee responsible for the development of the new curriculum for the Secondary Technical Education for the areas of computer systems, computer networks and automation systems
5	2011 - 2012	Ministry of Education and Culture of Cyprus	Chair of a committee responsible for the development of the curriculum for the Post-Secondary Institutes for Vocational Education (MIEEK) for the areas of communications, computer networks and automation systems