



Academic Personnel Short Profile / Short CV

University:	Frederick University
Surname:	Tatas
Name:	Konstantinos
Rank/Position:	Assistant Professor
School:	Engineering
Department:	Electrical Engineering, Computer Engineering and Informatics
Scientific Domain:	Computer Architecture and Embedded Systems

Academic qualifications

Qualification	Year	Awarding Institution	Department	Thesis title (Optional Entry)
PhD	2005	Democritus University of Thrace, Greece	Electrical and Computer Engineering	VLSI Design of DSP Systems with Low Power Consumption
BSc	1999	Democritus University of Thrace, Greece	Electrical and Computer Engineering	Design and Implementation of Arithmetic Units using VHDL

Employment history in Academic Institutions/Research Centers

Period of employment		Employer	Location	Position
From	To			
2007	Now	Frederick University	Nicosia	Lecturer/Assistant Professor
2007	2007	Frederick Institute of Technology	Nicosia	Lecturer
2006	2007	Alexander Technological Institute of Thessaloniki	Thessaloniki, Greece	Laboratory Instructor
2000	2003	INTRACOM SA Research Center	Xanthi/Athens, Greece	Digital Integrated Circuit Designer, Researcher

Recent refereed journal papers, monographs, books, conference publications etc. (up to five until May 2021)

Ref. Number	Year	Title	Other authors	Journal and Publisher / Conference	Vol.	Pages
1	2021	Towards an Analytical Model of Latency in Deflection Routing: A Stochastic Process Approach for Bufferless NoCs	–	10th International Conference on Modern Circuits and Systems Technologies (MOCASST 2021) (to appear)		
2	2021	iPONICS: IoT Monitoring and Control for Hydroponics	Ahmad Al-Zoubi, Diana Zolotareva, Anthony Antoniou	10th International Conference on Modern Circuits and Systems Technologies (MOCASST 2021) (to appear)		
3	2020	Fuzzy classification of OpenCL programs targeting heterogeneous systems	Ahmad Al-Zoubi, Costas Kyriacou	Journal of Intelligent & Fuzzy Systems, IOS Press	39(5)	7189-7202
4	2020	Rapid High-Level FPGA Resource Estimation for a Novel Heterogeneous Platform Scheduling Scheme	Ahmad Al-Zoubi	11th International Conference on Information and Communication Systems (ICICS 2020)		
5	2018	Towards Dynamic Multi-task Scheduling of OpenCL Programs on Emerging CPU-GPU-FPGA Heterogeneous Platforms: a Fuzzy Logic Approach	Ahmad Al-Zoubi, Costas Kyriacou	10th IEEE International Conference on Cloud Computing Technology and Science (CloudCom 2018)		

Key refereed journal papers, monographs, books, conference publications etc. (up to five until May 2021)

Ref. Number	Year	Title	Other authors	Journal and Publisher / Conference	Vol.	Pages
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1	2017	Hardware implementation of dynamic fuzzy logic based routing in Network-on-Chip	Chrysostomos Chrysostomou	Microprocessors and Microsystems - Embedded Hardware Design (Elsevier)	52	80-88
2	2014	Designing 2D and 3D Network-on-Chip Architectures	Kostas Siozios, Dimitrios Soudris, Axel Jantsch	Springer		
3	2007	Architecture design of a coarse-grain reconfigurable multiply-accumulate unit for data-intensive applications	George Koutroumpetis, Dimitrios Soudris, Antonios Thanailakis	Integration, the VLSI Journal, Elsevier	40(2)	74-93
4	2006	Behavioral-Level Performance and Power Exploration of Data-Intensive Applications Mapped on Programmable Processors	Nikolaos Kroupis, Nikolaos D. Zervas, Minas Dasygenis, Antonios Thanailakis	Journal of VLSI Signal Processing, Springer	44	153-171
5	2005	A complete platform and toolset for system implementation on fine-grain reconfigurable hardware	Vasilis Kalenteridis, H. Pournara, Kostas Siozios, Dimitrios Soudris, Antonios Thanailakis	Microprocessors and Microsystems, Elsevier	29(6)	247-259

Research Projects (up to ten until May 2021)				
Ref. Number	Date	Title	Funded by	Project Role
1	2019-2021	iPONICS: SmartOff-Grid System for Sustainable Hydroponics	Cyprus Research and Innovation Foundation	Scientific Coordinator
2	2010-2012	SYStematic design of 3D Multicore Architectures with NeTwork-on-chip Communication (SYSMANTIC)	Cyprus Research Promotion Foundation	Principal Researcher
3	2005-2008	DDM-CMP: Data Driven Multithreading on a Chip Multiprocessor, Development and Implementation	Cyprus Research Promotion Foundation	Researcher
4	2002-2005	AMDREL: "Architectures and Methodologies for Dynamic REconfigurable Logic	EC 5th IST Framework	Researcher

5	2001-2004	EASY: Energy-Aware SYstem-on-chip design of the HIPERLAN/2 standard	EC 5th IST Framework	Researcher
6	2004-2005	Wireless Subscriber's Connections of Advanced Services Access Network Implemented in Silicon (MILI-A, Wireless mile	General Secretariat for Research and Technology - Greece.	Researcher
7	2001-2003	Memory Management Methodology for Real-Time and Low-Power Consumption Embedded Multimedia Systems	General Secretariat for Research and Technology - Greece	Researcher

Academic Consulting Services and/or Participation in Councils / Boards/ Editorial Committees				
Ref. Number	Period	Organization	Title of Position or Service	Key Activities
1	2018-2020	Cyprus Ministry of Transport, Communications and Works	Advisor	Co-author of Cyprus national strategy for Artificial Intelligence