Academic Personnel Short Profile

University:	Frederick University Cyprus		
Reg. ID:*	622		
Surname:	Savva		
Name:	vvas		
Rank:	Postgraduate Associate		
School:	Engineering		
Department:	Computer Science and Engineering		
Scientific Domain:**	Computer Architecture, Digital System Design, VLSI, NOC		

*Registration ID as provided by the University
** Field of Specialization

Academic qualifications (list by highest qualification)				
Qualification	Year	Awarding Institution	Department	Thesis title
PhD Candidate	Present	Frederick University	Computer Science and Engineering	Fault-tolerant routing methodology for Networks-on-Chip
MSc	2013	Frederick University	Electrical Engineering	A Framework for Networks- on-Chip (NoC) Self-Checking Testbenches
BSc	2011	Frederick University	Computer Science and Engineering	Automatic Network-on-Chip Testbench Generation CAD Tool

Academic Staff Short Profile

Employment history – List by the three (3) most recent					
Period of employment		Employer			
From	То		Location	Position	
Sep 2013	Present	Frederick University	Nicosia, Cyprus	Postgraduate Associate	
Jul 2018	Oct 2018	8 RunStop Electronics Paphos, Cyprus Co Ltd		Network and Computer Technician	
Jun 2010	Aug 2010	Electricity Authority Cyprus	Paphos, Cyprus	Employee at Drawing Office	

Key <u>refereed</u> journal papers, monographs, books, conference publications etc. List the five (5) more recent and other ten (10) selected – <i>categories may overlap.</i> (max total 15)						
Ref. Number	Year	Title	Other authors	Journal / Conference/ Publisher	Vol.	Pages
1	2017	Bufferless Hybrid Router for 3D Networks-on-Chip	K. Tatas, C. Kyriacou	27 th International Symposium on Power and Timing Modeling, Optimization and Simulation (PATMOS 2017)	-	1-8
2	2017	Fault-tolerant routing methodology for Networks-on-Chip	-	27th International Symposium on Power and Timing Modeling, Optimization and Simulation (PATMOS 2017)	-	1-3

Academic Staff Short Profile 2

3	2014	Low-Cost Fault-Tolerant	K. Tatas, C. Kyriacou	21st IEEE	-	566-
		Routing for Regular Topology		International		569
		NoCs		Conference on		
				Electronics Circuits		
				and Systems		

*Project Role: i.e. Scientific/Project Coordinator, Research Team Member, Researcher, Assistant Researcher

Awards	Awards / International Recognition (where applicable). List the five (5) more recent and other five (5) selected. (max total 10)				
Ref. Number	Date	Title	Awarded by:		
1	27/9/2017	Best Ph.D. Forum Poster	27 th International Symposium on Power and Timing Modeling, Optimization and Simulation (PATMOS 2017)		

Academic Staff Short Profile 3